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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 1

Complete if Known

Application Number	10/612,729
Filing Date	7-2-2003
First Named Inventor	Chan
Group Art Unit	2157
Examiner Name	Not yet assigned
Attorney Docket Number	14227US01

U.S. PATENT DOCUMENTS

Examiner Initial*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
MD	A1	US-6,175,865 A	01-16-2001	Dove et al	
MD	A2	US-5,923,663 A	07-13-1999	Bontemps et al	
MD	A3	US-2002/ 023234 A1	02-21-2002	Berman et al	
	A4				
	A5				
	A6				
	A7				
	A8				
	A9				
	A10				
	A11				
	A12				

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ² -Number ³ -Kind Code ⁴ (if known)				
MD	A13	EP-0863640 A	09-09-1998	Texas Instruments Inc.		
	A14					
	A15					
	A16					
	A17					

OTHER ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	A18	
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EXAMINER
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/Mon Cheri Davenport/

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FORM PTO-1449	SERIAL NO. 10/612,729	CASE NO. 14227US01
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE July 2, 2003	GROUP ART UNIT 2157
(use several sheets if necessary)		APPLICANT(S): Kevin T. Chan

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)
MD	Huang et al. "Simulated Performance of 1000BASE-T Receiver with Different Analog Front End Designs", Solid-State Circuits Research laboratory Department of Electrical and Computer Engineering University of California, Davis, Davis, CA 95616 (4 pages)

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